

Docket No.: 49657-875



#15/8-5-04
D
v. 5024

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
Tetsushi TANIZAKI, et al.	:	Confirmation Number: 8725
Serial No.: 09/712,246	:	Group Art Unit: 2133
Filed: November 15, 2000	:	Examiner: M.M. Chaudry
For: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT OPERATING AT HIGH RATE	:	

AMENDMENT

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

Sir:

The following amendment and remarks are submitted in response to the final Office

Action dated December 29, 2003.

A copy of the claims that remain pending with this proposed response begins on
page 2 of this paper.

Remarks begin on page 11 of this paper.